

Key Features

The transceiver complies with common management interface specification (CMIS). The supported key features listed below allow host software to read and control the transceiver status through I2C.

- Adaptive Tx input equalization
- Programmable Rx output amplitude
- Programmable Rx output pre-cursor
- Programmable Rx output post-cursor
- Supply voltage monitoring (DDM_Voltage)
- Transceiver case temperature monitoring (DDM_Temperature)
- Tx transmit optical power monitoring for each lane (DDM_TxPower)
- Tx bias current monitoring for each lane (DDM_TxBias)
- Rx receive optical power monitoring for each lane (DDM_RxPower)
- Warning and alarm indication for each DDM function
- Tx & Rx LOL and LOS indication
- Tx fault indication
- Host and line side loopback capabilities
- Host and line side PRBS generator and checker capabilities
- CDB firmware upgrade capability
- Versatile diagnostics monitoring (VDM) capability (optional, additional power consumption increase)
- Other functions defined in CMIS

Applications

The transceiver is designed for Ethernet, Telecom and Infiniband use cases. The application advertisements listed below allow host software to select proper application following CMIS definition Table 1 shows CMIS application advertisements list:

DsSel Code	K r w Electrical Interface	P r g x o h Media Interface	K r w d q g Media Odqh Count	K r w Lane Dssignment
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Pin Map and Description

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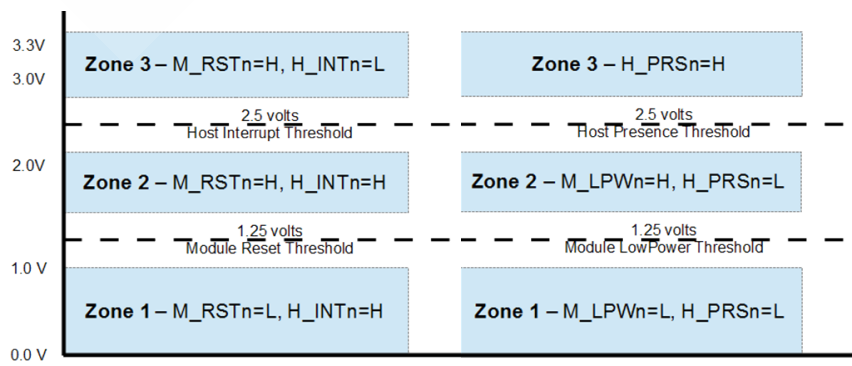


HH	w·HL	wS0Si0SN 5I-úI b2yñLy0SNúSR hdzúLJdzú	/ a [πh	O
HO	Db5		Dñ2dzyR	M
HP	w·nY	wS0Si0SN 5I-úI Ly0SNúSR hdzúLJdzú	/ a [πh	O
HP	w·nL	wS0Si0SN 5I-úI b2yñLy0SNúSR hdzúLJdzú	/ a [πh	O
HC	Db5		Dñ2dzyR	M
HT	a2Rtñäf	a2RdzfS tñSäSyú	[±cc[πh	O
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OL	±00M	bo±o± t20SN {dzLJfē		H
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OH	Db5		Dñ2dzyR	M
OO	·oL	ññI-yāYñúúSN 5I-úI b2yñLy0SNúSR LyLJdzú	/ a [πL	O
OP	·oY	ññI-yāYñúúSN 5I-úI Ly0SNúSR LyLJdzú	/ a [πL	O
OP	Db5		Dñ2dzyR	M
OC	·mL	ññI-yāYñúúSN 5I-úI b2yñLy0SNúSR LyLJdzú	/ a [πL	O
OT	·mY	ññI-yāYñúúSN 5I-úI Ly0SNúSR LyLJdzú	/ a [πL	O
OY	Db5		Dñ2dzyR	M

ñI-öfS o äK20ä üKŠ ŘŠúI-ñfŠŘ özyúñ2f Lñyā

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Name	Direction	Description
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Lbñkw{ñy	LyLJdzúkh dzúLJdzú	5dzI-f Cdzýüñ2y' {ñyI-f · wSäSü lä I-yI-öüñöSñf20 LyLJdzú äñyI-f · LyúSññdzLúü lä I-yI-öüñöSññKñK 2dzúLJdzú äñyI-f ±2ñI-ŠS İzyŠä lä äK20yI-ä ññdzññS oñ



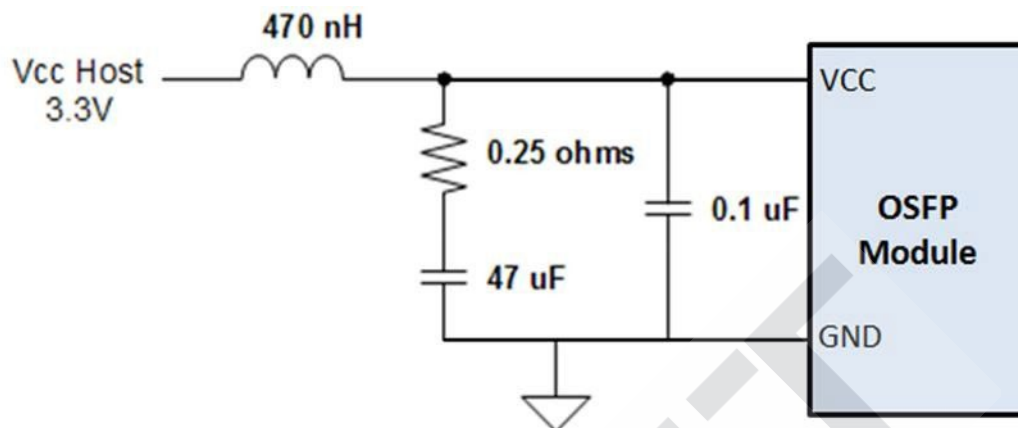
ññdzññS oñ Voltage Zones



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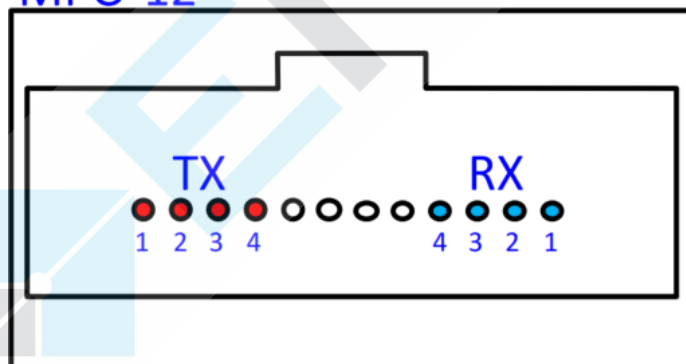


ChadziNS n wŠ02YYŠyŘŠŘ t20ŠN {dzLJLfè CafúŠN

Optical Port Description

ČKS 2LJú0I-f lyúŠNFI-0Š LJ2Nú á I-y athπMH NS0ŠLJúI-0tŠ ČKS úNI-yáYáú I-yŘ NS0ŠLJúI-0tŠ 2LJú0I-f fI-yŠá áKI-ft 200dzLJè úKS LJ2áú2yá ŘŠLJú0ŠŘ lyČadziNS n 0KSyf22lyy lyú2 úKS a 5L NS0ŠLJúI-0tŠ 0áúK úKS 02yyŠ0ú2N lŠè0I-è FŠI-údzNú 2yú2LJ0

MPO-12



ChadziNS p hLJú0I-f a ŠŘlI- 5ŠLJŠyŘŠyú lyúŠNFI-0Š LJ2Nú I-ááEYŠyúá

Absolute Maximum Ratings

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5ɪ-úɪ- wɪ-úŠΣ Šɪ-òK [ɪ-yŠ			poʰmnp		D.Ř	t! a n
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Optical Characteristics

Parameter	Symbol	Unit	Min	Typ	Max	Test Conditions
Optical Characteristics						
Wavelength	λ	nm	1304.5	1310	1317.5	25°C
Side-mode suppression ratio	SMSR	dB			30	
Average Launch Power, each Lane	P_{AVG}	dBm	-2.9		4	
Outer Optical Modulation Amplitude (OMA _{outer}), each Lane	P_{OMA}	dBm	-0.8		4.2	
Launch power in OMA _{outer} minus TDECQ, each Lane	OMA-TDECQ	dBm	-2.2			
Transmitter and Dispersion Eye Closure for PAM4 (TDECQ), each Lane	TDECQ	dB			3.4	
TDECQ – 10log ₁₀ (Ceq)		dB			3.4	
Extinction Ratio	ER	dB	3.5			
Transition Time	T_t	ps			17	
Average Launch Power of OFF Transmitter, each Lane	P_{off}	dBm			-15	
RIN _{21.4OMA}	RIN	dB/Hz			-136	
Optical Return Loss Tolerance	TOL	dB			21.4	
Transmitter reflectance	T_r	dBm			-26	

Receiver						
Data Rate, each Lane		53.125 ± 100 ppm			GBd	
Modulation Format		PAM4				
Center wavelength	λ	1304.5		1317.5	Nm	
Damage Threshold, each Lane	TH _d	5			dBm	3
Average Receive Power, each Lane		-5.9		4	dBm	4
Receive Power (OMA _{outer}), each Lane				4.2	dBm	
Receiver Reflectance	R _R			-26	dB	
Receiver Sensitivity (OMA _{outer}), each Lane	SEN			Equation (1)	dBm	5
Stressed Receiver Sensitivity (OMA _{outer}), each Lane	SRS			-1.9	dBm	6
LOS Assert	LOSA	-15			dBm	
LOS De-assert	LOSD			-9.2	dBm	
LOS Hysteresis	LOSH	0.5			dB	
Conditions of Stress Receiver Sensitivity Test (Note 7)						
Stressed Eye Closure for PAM4 (SECQ), Lane under Test			3.4		dB	
SECQ – 10log ₁₀ (Ceq), lane under test			3.4		dB	
OMA _{outer} of each aggressor lane			4.2		dBm	

Notes:

1. Average launch power, each lane (min) is informative and not the principal indicator of signal strength. A transmitter with launch power below this value cannot be compliant; however, a value above this does not ensure compliance.
2. Even if the TDECQ < 1.4 dB, the OMA_{outer}(min) has to exceed this value.
3. The receiver shall be able to tolerate, without damage, continuous exposure to a modulated optical input signal having this power level on one lane. The receiver does not have to operate correctly at this input power.
4. Average receive power, each lane (min) is informative and not the principal indicator of signal strength. A received power below this value cannot be compliant; however, a value above this does not ensure compliance.
5. Receiver sensitivity (OMA_{outer}) is informative and is defined for a transmitter with a value of SECQ up to 3.4 dB. Receiver sensitivity should meet Equation (1), which is illustrated in Figure 6.

$$RS = \max (-3.9, SECQ - 5.9) \text{ dBm} \quad (1)$$

Where:

RS is the receiver sensitivity, and

SECQ is the SECQ of the transmitter used to measure the receiver sensitivity.

6. Measured with conformance test signal at TP3 for the BER equal to 2.4×10^{-4} .
7. These test conditions are for measuring stressed receiver sensitivity. They are not characteristics of the receiver.

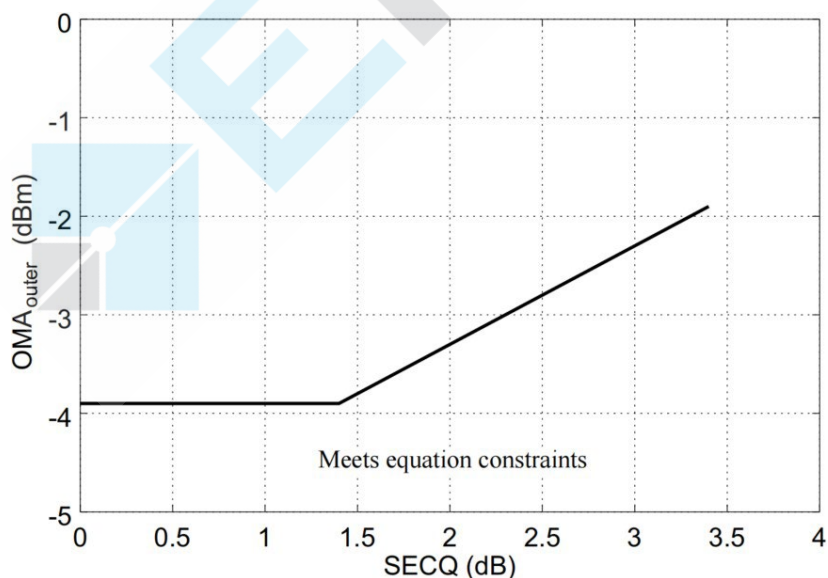


Figure 6. Illustration of Receiver Sensitivity Mask for 100GBASE-FR1

Digital Diagnostic Specifications

The following digital diagnostic characteristics are defined over the normal operating conditions unless otherwise specified.

Parameter	Symbol	Min	Max	Units	Notes
Temperature monitor absolute error	DMI_Temp	-3	3	degC	Over operating temperature range
Supply voltage monitor absolute error	DMI_VCC	-0.1	0.1	V	Over full operating range
Channel RX power monitor absolute error	DMI_RX_Ch	-2	2	dB	1
Channel Bias current monitor	DMI_Ibias_Ch	-10%	10%	mA	
Channel TX power monitor absolute error	DMI_TX_Ch	-2	2	dB	1

Notes:

1. Due to measurement accuracy of different single mode fibers, there could be an additional +/- 1 dB fluctuation, or a +/- 3 dB total accuracy.

Mechanical Drawing

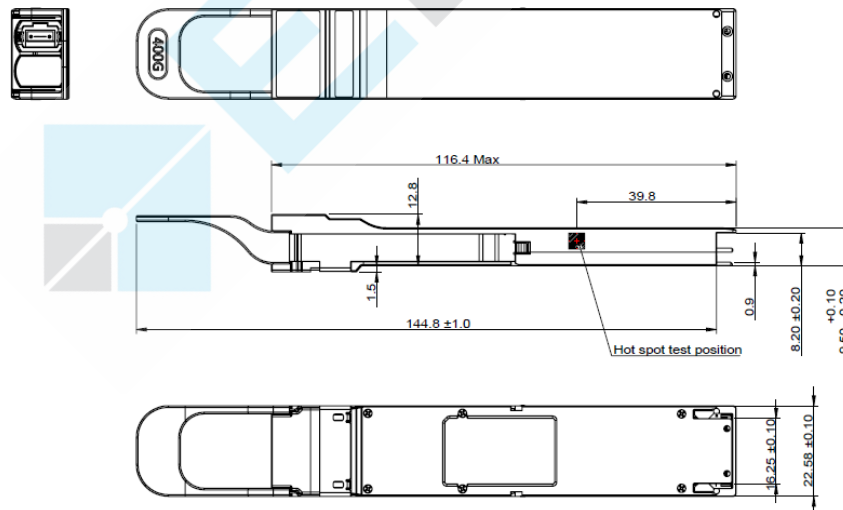


Figure 6. Mechanical Outline

Notes:

- 1) The mechanical design is flat top (RHS).
- 2) The pull tab color is green, engraved with "400G" letter.



ESD

This transceiver is specified as ESD threshold 1kV for high-speed data pins and 2kV for all other electrical input pins, tested per MIL-STD-883, Method 3015.4 /JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module. This transceiver is shipped in ESD protective packaging. It should be removed from the packaging and handled only in an ESD protected environment.

Laser safety

This is a Class I Laser Product, or Class 1 Laser Product according to IEC/EN 60825-1:2014. This product complies with 21 CFR 1040.10 and 1040.11 except for conformance with IEC 60825-1 Ed. 3., as described in Laser Notice No. 56, dated May 8, 2019.

Caution: Use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.